

Altium™

Altium Academic Roadshow: 'Designing PCBs Faster' 11 June 2015

Agenda

09:00 Registration

09:30 Welcome / Introduction

- Schematic Entry (Altium)

 - High-level features

 - Repeat

 - Busses/Harnesses

 - Re-use

 - Version control

- Vaults/Library Management (Altium)

 - Database Library

 - Vaults Library Management

 - Supplier links

 - Release management

- Analog simulation - PCB Design (Altium)

 - Integration ECAD - MCAD 3D

 - Release Configurations and Altium Vaults

 - Flex and Flex-Rigid designs

 - High Density/High speed/Signal Integrity

 - Output Job files

 - 3D PDF

12:30 Lunch

- Introduction Boundary-scan (JTAG)

 - Test generation

- Boundary-scan application development using ProVision (JTAG)

 - Testability check

 - First prototype test in the engineering phase

 - Hardware validation

 - Production Test

16:45 Questions and answers

